



# 产品承认书

## Specification for Approval

客户 : \_\_\_\_\_  
Customer : \_\_\_\_\_  
客户料号 : \_\_\_\_\_  
Customer P/N : \_\_\_\_\_  
物料名称 : **一体成型电感**  
Item Name : **Molding Power Choke**  
金籁料号 : **LPB201610D 系列**  
Jinlai P/N : \_\_\_\_\_  
承认书编号 : **JL-APR-MOD-3787**  
Spec No. : \_\_\_\_\_  
版本号 : **A0**  
Version No. : \_\_\_\_\_

制造商 Manufacture	
拟制 Prepared	谢奕君
审核 Checked	陈洁新
批准 Approved	村上
签章 Signature	
日期 Date	2023-03-09

客户承认 Approved by Customer	
确认 Confirmed	
审核 Checked	
批准 Approved	
签章 Signature	
日期 Date	

备注:  
Remark

- 在使用产品前, 用户必须确认此产品是否适用于自身设计, 金籁仅保证产品符合此份承认书的规格。  
Before use, customer should confirm whether this product is suitable for their design, JinLai only ensure products meet this specification.
- 本承认书的数据更改, 必须经双方确认, 任何一方单独修改无效。  
This specification data change must be confirmed by both parties, any individual modification is invalid.
- 如客户未回签承认书即下订单, 则视为承认此份承认书。  
If customer placed orders without signing back this specification, it is regarded as recognition.

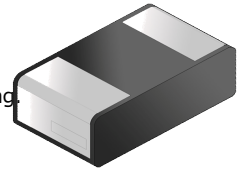
重庆金籁科技股份有限公司  
ChongQing JinLai Technology Corp.,

重庆市丰都县水天坪工业园区  
Shuitianping industrial park, fengdu county, chongqing  
Tel: (023)64599888 Fax:(023)81850171

# Power Inductor LPB201610D

## 1. 特征 / Features

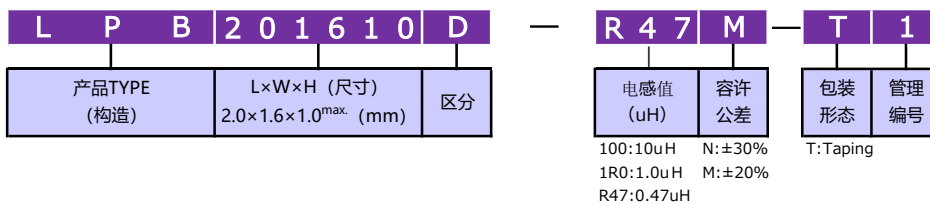
- 采用金属磁性材料具有高磁通饱和密度特性  
High magnetic flux saturation density characteristics by metal magnetic material.
- 采用扁平线材实现较低Rdc  
Low DC resistance by flat wire and achieve high conversion efficiency and lower temperature rising.
- 通过闭磁路结构减少漏磁通  
Magnetically shielded structure to accomplish high resolution in EMC protection.
- Chip形状, 贴装稳定性强  
High mounting stability due to Chip shape.
- 具有独立结构, 可靠性高  
High reliability by original structure.
- 无卤素、无铅化, 符合RoHS要求  
Halogen free, Lead free, RoHS Compliance.



## 2. 应用 / Application

- DC/DC converter
- Smart phone/PAD, HDD/SSD, DVC/DSC
- mobile display panels, portable game devices, compact power supply modules, other.

## 3. 型号一览 / Part Number Construction



## 4. 特性规格表 / Characteristics Specification Table

客户料号 Customer's Part No.	品名 Part No.	L (uH)	公差范围 Tolerance	直流电阻Rdc (mΩ) Max.[Typ.]	饱和电流 I <sub>sat</sub> (A) Max.[Typ.]	温升电流 I <sub>temp</sub> (A) Max.[Typ.]	备注 Remarks
	LPB201610D-R24M-T1	0.24	±20%	23.5 [19.5]	6.10 [7.20]	4.25 [5.00]	
	LPB201610D-R33M-T1	0.33	±20%	27 [22.5]	5.75 [6.80]	4.05 [4.75]	
	LPB201610D-R47M-T1	0.47	±20%	34 [29.5]	4.90 [5.80]	3.65 [4.20]	
	LPB201610D-R68M-T1	0.68	±20%	43 [36]	3.65 [4.30]	3.10 [3.65]	
	LPB201610D-R82M-T1	0.82	±20%	53 [45]	3.50 [4.10]	2.80 [3.30]	
	LPB201610D-1R0M-T1	1.0	±20%	62 [54]	3.40 [4.00]	2.70 [3.10]	
	LPB201610D-1R5M-T1	1.5	±20%	89 [77]	2.70 [3.20]	2.15 [2.45]	
	LPB201610D-2R2M-T1	2.2	±20%	147 [125]	2.35 [2.80]	1.70 [2.00]	
	LPB201610D-3R3M-T1	3.3	±20%	200 [170]	1.70 [2.00]	1.35 [1.60]	
	LPB201610D-4R7M-T1	4.7	±20%	318 [270]	1.55 [1.85]	1.05 [1.25]	
	LPB201610D-6R8M-T1	6.8	±20%	485 [410]	1.25 [1.50]	0.75 [0.88]	

·L测定频率: 1MHz, OSC LEVEL:0.5V / Test frequency at 1MHz, OSC LEVEL:0.5V

·L测定器: 4285A (Keysight Technologies) 或同等测定器

Inductance is measured with Keysight Technologies LCR meter 4285A. Equivalent measurement equipment may be used.

·额定电流: 以Isat和Itemp两个中的一个较小值作为额定电流值 / Rated current: smaller value of either Isat or Itemp.

·Isat: 电感量比初始值下降约30%时所加载的直流电流值

Isat: The DC current at which the inductance decreases approximately 30% from the actual initial value.

·Itemp: 基于温度上升的情况(自身发热温度上升40°C)/Itemp: When based on the temperature increase(temperature increase of 40°C by self heating).

※Itemp是基于本公司的使用环境的参考值 / Itemp is a reference value according to our usage environment.

※在最终应用中, 必须在产品温度上升不超过40°C的条件下进行电路设计

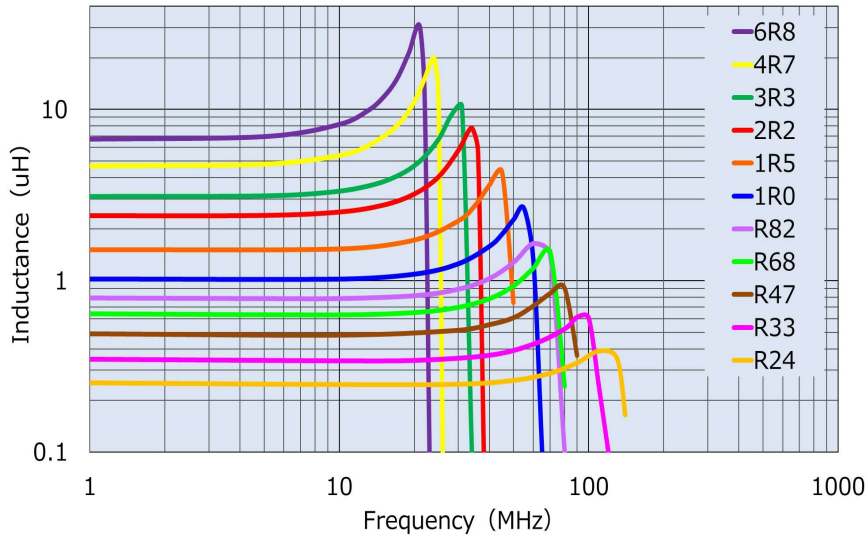
In the end application, the circuit design should be such that the part temperature rise does not exceed 40 °C.

·最大电压: 20[V] / Withstand DC Voltage: 20[V]



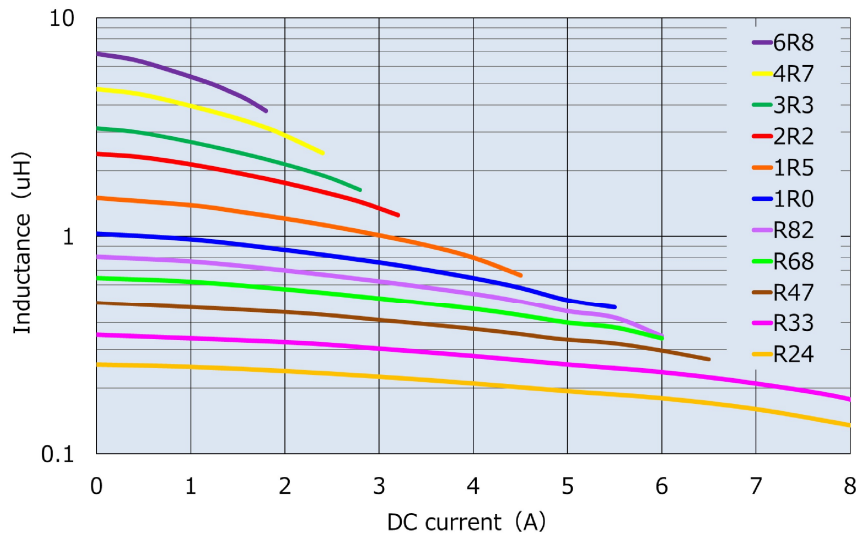
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## 5. L频率特性 / L Frequency Characteristics



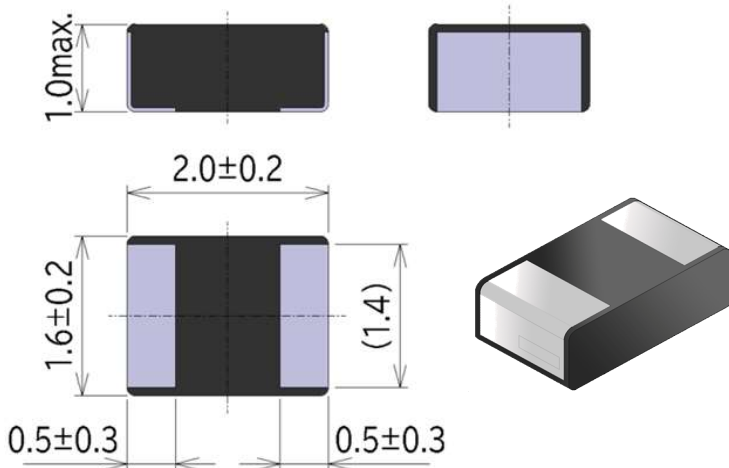
测定器: 4294A (Keysight Technologies)  
或同等测定器

## 6. L直流叠加特性 / Inductance vs. DC Bias Characteristics



测定器: 4285A + 42841A + 42842C  
(Keysight Technologies)  
或同等测定器

## 7. 形状 / Shape & Dimensions

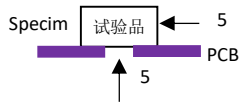
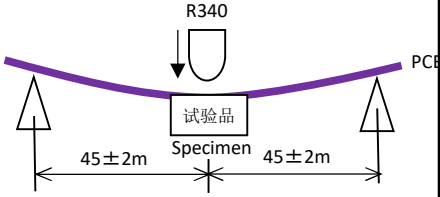


尺寸单位: mm  
Dimensions in : mm



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## 11. 可靠性试验 / Reliability and Test Condition

试验项目/Test item	试验条件/Test condition	规格/Specification
固着强度 Adhesive Test	<ul style="list-style-type: none"> <li>· 负荷时间: 10±1秒 / Test time:10±1sec.</li> <li>· 移除负荷后进行测量。 Measure after removing pressure.</li> </ul> 	<ul style="list-style-type: none"> <li>· L变化率: ±10%以内/ ΔL/L: within ± 10% (以初期L为基准) (Change from an initial value)</li> <li>· 试验后的外观无异常 No abnormal appearance after the test.</li> </ul>
基板弯曲试验 Bending test	<ul style="list-style-type: none"> <li>· 弯曲量: 2mm / Bent depth: 2mm</li> <li>· 基板size: 40×100mm / PCB size:40×100mm</li> <li>· 基板厚度: 1.0mm / PCB thickness:1.0mm</li> <li>· 保持时间: 30秒 / Test time:30sec.</li> </ul> 	<ul style="list-style-type: none"> <li>· L变化率: ±10%以内/ ΔL/L: within ± 10% (以初期L为基准) (Change from an initial value)</li> <li>· 试验后的外观无异常 No abnormal appearance after the test.</li> </ul>
耐振动性 Vibration test	<ul style="list-style-type: none"> <li>· 扫描频率: 10~55~10Hz/分 Sweep frequency: 10~55Hz(10Hz to 55Hz to 10Hz in a period of one minute)</li> <li>· 全振宽度: 1.5mm / Amplitud: 1.5mm</li> <li>· X,Y,Z方向各2Hr (共6Hr) 2Hr in each of 3(X, Y, Z) axes.</li> </ul>	<ul style="list-style-type: none"> <li>· L变化率: ±10%以内/ ΔL/L: within ± 10% (以初期L为基准) (Change from an initial value)</li> <li>· 试验后的外观无异常 No abnormal appearance after the test.</li> </ul>
耐冲击性 Mechanical shock test	<ul style="list-style-type: none"> <li>· 加速度: 1962m/s<sup>2</sup> Peak acceleration: 1962m/s<sup>2</sup></li> <li>· 作用時間: 6ms / Duration of pulse: 6ms</li> <li>· X,Y,Z方向各3次 (共9次) 3 times in each of 3(X, Y, Z) axes.</li> </ul>	<ul style="list-style-type: none"> <li>· L变化率: ±10%以内/ ΔL/L: within ± 10% (以初期L为基准) (Change from an initial value)</li> <li>· 试验后的外观无异常 No abnormal appearance after the test.</li> </ul>
跌落试验 Drop test	<p>待如表有试验品的基板外表任意位置为500g的器具上, 从1m高处向相互垂直的3个方向自然落下在坚硬的木板上, 各3次 (共9次)。</p> <p>The specimen must be fixed on PCB. It must be equipped with instruments of which weight is 500g. Then it shall be fallen freely from 100cm height to rigid wood 3 times in each of three axes.</p>	<ul style="list-style-type: none"> <li>· L变化率: ±10%以内/ ΔL/L: within ± 10% (以初期L为基准) (Change from an initial value)</li> <li>· 试验后的外观无异常 No abnormal appearance after the test.</li> </ul>
耐焊锡热 Resistance to Solder Heat	<ul style="list-style-type: none"> <li>· Reflow焊锡/Reflow soldering method Preheat: 150~180°C 90±30sec. Peak temp.: 255(+5/-0)°C (230°Cmin.,30±10sec.)</li> <li>· 基板厚度: 1.0mm / PCB thickness:1.0mm</li> <li>· 2回 / 2times</li> </ul>	<ul style="list-style-type: none"> <li>· L变化率: ±10%以内/ ΔL/L: within ± 10% (以初期L为基准) (Change from an initial value)</li> <li>· 试验后的外观无异常 No abnormal appearance after the test.</li> </ul>
可焊性 Solder ability	<ul style="list-style-type: none"> <li>· 电极在室温下涂上焊剂后根据下述条件将试验样品整体浸于焊锡槽中。 Electrode shall be immersed in flux at room temperature and then shall be immersed in solder bath after preheat.</li> <li>· 焊锡温度: 245±5°C、浸锡时间: 3±0.5sec. Solder temp:245±5°C、Dip time:3±0.5sec.</li> </ul>	<ul style="list-style-type: none"> <li>· 电极面 90%以上覆盖新的焊料。 New solder shall cover 90% minimum of the surface immersed.</li> </ul>
温度特性 Temperature drift	<ul style="list-style-type: none"> <li>· 温度范围: -40~+125°C To be measured in the range of -40°C to 125°C.</li> </ul>	<ul style="list-style-type: none"> <li>· Inductance温度系数: 1000ppm/°C以下 Inductance temperature coefficient 1000ppm/°C or less.</li> </ul>

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试验项目/Test item	试验条件/Test condition	规格/Specification															
耐寒试验 Low temperature test	· 温度: $-40\pm 3^{\circ}\text{C}$ / Temp.: $-40\pm 3^{\circ}\text{C}$ · 放置时间: $500\pm 12\text{Hr}$ / Load: $500\pm 12\text{Hr}$	· L变化率: $\pm 10\%$ 以内/ $\Delta L/L$ : within $\pm 10\%$ (以初期L为基准) (Change from an initial value) · 试验后的外观无异常 No abnormal appearance after the test.															
耐热试验 Dry heat test	· 温度: $+85\pm 2^{\circ}\text{C}$ / Temp.: $+85\pm 2^{\circ}\text{C}$ · 放置时间: $500\pm 12\text{Hr}$ / Load: $500\pm 12\text{Hr}$	· L变化率: $\pm 10\%$ 以内/ $\Delta L/L$ : within $\pm 10\%$ (以初期L为基准) (Change from an initial value) · 试验后的外观无异常 No abnormal appearance after the test.															
耐湿试验 Humidity test	· 温度: $+40\pm 2^{\circ}\text{C}$ / Temp.: $+40\pm 2^{\circ}\text{C}$ · 湿度: $90\sim 95\%RH$ / Humidity: $90\sim 95\%$ · 放置时间: $500\pm 12\text{Hr}$ / Load: $500\pm 12\text{Hr}$	· L变化率: $\pm 10\%$ 以内/ $\Delta L/L$ : within $\pm 10\%$ (以初期L为基准) (Change from an initial value) · 试验后的外观无异常 No abnormal appearance after the test.															
热冲击试验 Thermal shock test	· 1循环条件 / Condition of 1 cycle <table border="1" data-bbox="564 1055 916 1305"> <thead> <tr> <th>Step</th> <th>温度 (<math>^{\circ}\text{C}</math>) Temp.</th> <th>时间 (min.) Duration</th> </tr> </thead> <tbody> <tr> <td>1</td> <td><math>-40\pm 3</math></td> <td><math>30\pm 3</math></td> </tr> <tr> <td>2</td> <td>常温 Room temp.</td> <td>3以下 Within 3</td> </tr> <tr> <td>3</td> <td><math>+125\pm 2</math></td> <td><math>30\pm 3</math></td> </tr> <tr> <td>4</td> <td>常温 Room temp.</td> <td>3以下 Within 3</td> </tr> </tbody> </table> · 放置时间: $100\pm 12\text{cycle}$ /Load: $100\pm 12\text{cycle}$	Step	温度 ( $^{\circ}\text{C}$ ) Temp.	时间 (min.) Duration	1	$-40\pm 3$	$30\pm 3$	2	常温 Room temp.	3以下 Within 3	3	$+125\pm 2$	$30\pm 3$	4	常温 Room temp.	3以下 Within 3	· L变化率: $\pm 10\%$ 以内/ $\Delta L/L$ : within $\pm 10\%$ (以初期L为基准) (Change from an initial value) · 试验后的外观无异常 No abnormal appearance after the test.
Step	温度 ( $^{\circ}\text{C}$ ) Temp.	时间 (min.) Duration															
1	$-40\pm 3$	$30\pm 3$															
2	常温 Room temp.	3以下 Within 3															
3	$+125\pm 2$	$30\pm 3$															
4	常温 Room temp.	3以下 Within 3															

### 12. 温度范围 / Temperature Range

· 使用温度范围:  $-40\sim +125^{\circ}\text{C}$  / Operating Temperature range:  $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$

※使用温度范围包括自我温度上升 / ※Operating temperature range includes self-temperature rise.

※部件温度 (周围温度 + 温度上升) 在最恶劣的操作条件下, 不得超过 $125^{\circ}\text{C}$ 。

电路设计、组件布局、PCB迹线尺寸/厚度、气流和其他冷却条件都会影响部件温度。

部件温度必须在最终应用中验证。

The part temperature (ambient + temp rise) should not exceed  $125^{\circ}\text{C}$  under worst case operating conditions.

Circuit design, component placement, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

· 保管温度范围:  $+5\sim +40^{\circ}\text{C}$  (Taping) / Taping Package Storage Temperature range:  $+5^{\circ}\text{C}$  to  $+40^{\circ}$

